

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Albert S. Weiner

Title: Non-volatile transistor memory array incorporating read-only elements with single mask set

Docket No.: 2800.450US1

Serial No.: 10/810,035

Filed: March 26, 2004

Due Date: July 21, 2009

Examiner: Thinh Nguyen

Group Art Unit: 2818

Customer No.: 76287

Confirmation No.: 8458

Notice of Allowance Date: April 21, 2009

Mail Stop Issue Fee

Commissioner for Patents

P.O. Box 1450

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